



DFTdriver.vi

C:\Users\jimp\CLASS\codesign\labs\lab3\labview\DFTdriver.vi

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VISA Res Name: COM1
 InputFile: C:\Users\jimp\CLASS\codesign\labs\lab3\labview\triangle3_512pts.xy

baud rate: 19200
 Vals in file: 512, NumValsToWrt: 512, WrtStr: -127, NumWrtn: 511, NumValsToRead: 512, TotLinesRead: 513, BytesReadLoop: 14, DataStr: -133169152, -108105384

data bits: 8
 parity: None
 stop bits: 1.0
 flow control: None

IntBitWidth: 8
 SinCosMult: 4096
 Mult: 0.0039216, Zero: 0.498043

DelAfterWrt: 1
 y_int: -127, 0
 Freq: 0, 0.5, 1, 1.5, 2, 2.5, 3, 3.5

FPGA Vals	Float Vals	Converted	Real	Mag	Imag	Phase
-133169152	-32512	127.499	127.499	0.249022	0	0
-108105384	-26392.9	-103.502	-103.502	0.404314	0.635065	-179.648
54382464	13277	52.0669	52.0669	0.203402	-0.638964	0.703097
-12306948	-3004.63	-11.7829	-11.7829	0.0460349	0.21689	-178.945
0	0	0	0	0	0	0
-4213604	-1028.71	-4.0342	-4.0342	0.015766	0.123829	-178.242
6037264	1473.94	5.78021	5.78021	0.0225942	-0.212911	2.10951
-2311688	-564.377	-2.21326	-2.21326	0.00865353	0.0951562	-177.538
0	0	0	0	0	0	0
-1267248	-309.387	-1.21329	-1.21329	0.00474664	0.067008	-176.839
2170112	529.812	2.07771	2.07771	0.00813136	-0.12764	3.51542
-957280	-233.711	-0.916521	-0.916521			
0	0	0	0			
-589796	-143.993	-0.564683	-0.564683			
1104800	269.727	1.05776	1.05776			
-525428	-128.278	-0.503056	-0.503056			
0	0	0	0			
-334976	-81.7812	-0.320713	-0.320713			
667024	162.848	0.638623	0.638623			

Magnitude Plot 0: Magnitude (voltage) vs Freq (0.0 to 140.0)

XYPhase Plot 0: Phase (degrees) vs Freq (0.0 to 140.0)

Error in: js code, ce 0

ErrorOut

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