

## **LAB Assignment #3 for Hardware/Software Codesign with FPGAs**

Assigned: Mon., Oct 17, 2011

Due: Mon., Oct 24, 2011

### **Description: Demo EDK tutorial 2 in class**

Create a slave peripheral (no interrupts) and modify its behavior to add '1' to the value written in its register by the microprocessor. The microprocessor c code should simply write a value, read it back immediately and print it (demonstrating that 1 has been added).

### **Laboratory Report Requirements:**

1) Be prepared to demonstrate your project in class.

Grading:

Proper operation: 100%