

LAB Assignment #4 for ECE 443

Assigned: Wed., Sept 23, 2009

Due: Mon., Sept. 30, 2009

Description: Test your adder on the FPGA

Use the code that I have on my web site to test the skw instructions (accomplished through push-buttons and LEDs). I'm still working on getting the UART code ready so this lab should be pretty simple. See the comments in the code.

Laboratory Report Requirements:

1) NO LAB report is required for this LAB -- only a demo in class