

## **LAB #8, NO assignment due**

### **Description: Create a project that reads and writes BRAM using the example C\_code and VHDL provided**

Create a project by adding the VHDL files to a project called GPIO\_BRAM in Vivado. Synthesize, implement and generate a bitstream. Run SDK and add the C\_code that I've supplied to an application called GPIO\_BRAM (you've done both of steps in previous labs). Run the application using the test\_data\_10vals.txt file. This application writes the values from the file through the GPIO to a state machine that stores them in BRAM. It then reads the values back and prints them to the screen.

This is not an assignment but I recommend that you make this work. You will be doing something similar in the project.