VHDL

VHSIC Hardware Description Language

VHSIC - Very High Speed IC (program)

IC - Integrated Circuits

(its a military language)

A language for
- Design,
- Documentation,
- Simulation,
- (and synthesis)

Very verbose, complex, and powerful

We will only use a small subset

We have our own compiler/simulator, developed at Pitt.

We will teach VHDL by examples

(Note:get your UNIX accounts)