Compilation and Simulation

The compiler and simulator only exist
On the unix computer. (unix, cisunx)

The major steps:

1) create a vhdl file: sam.vhdl

```
It will have one "top level"
entity - architecture pair

entity FOO is
....
end FOO;

architecture BAR of FOO is
....
end BAR;
```

2) compile it:

```
$PITT_VHDL/bin/vcomp
                    -efoo.bar sam.vhdl
```

3) simulate it:

```
$PITT_VHDL/bin/vsim sam.ivf
```