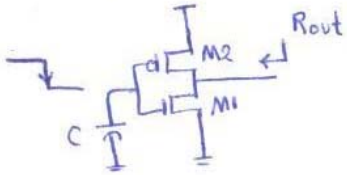


Problem 1:
Based on hint:

1. Connecting a load of 100pF to the gate



$$I_{av} = \frac{1}{2} [I_{ds}(V_{out}=0) + I_{ds}(V_{out} = \frac{V_{dd}}{2})]$$

2. Calculating the LH propagation delay

$$V_{out} = 0 \quad I_{ds} = \frac{k'_p}{2} \left(\frac{w}{L} \right)_p [V_{gs} - |V_{tp}|^2] (1 + \lambda V_{ds}) = 802 \mu A$$

$$V_{out} = \frac{V_{dd}}{2} \quad I_{ds} = \frac{k'_p}{2} \left(\frac{w}{L} \right)_p \left[2(V_{gs} - |V_{tp}|) \frac{V_{dd}}{2} - \frac{V_{dd}^2}{4} \right] = 555 \mu A$$

$$I_{av} = \frac{1}{2} (802 \mu A + 555 \mu A) = 678.5 \mu A$$

$$t_{pd} = \frac{C_L V_{dd}/2}{I_{av}} = \frac{(100 \text{ pF}) (1.5/2 \text{ V})}{678.5 \mu A} = 11.05 \text{ ns}$$

3. Equating propagation delay to a simple RC network

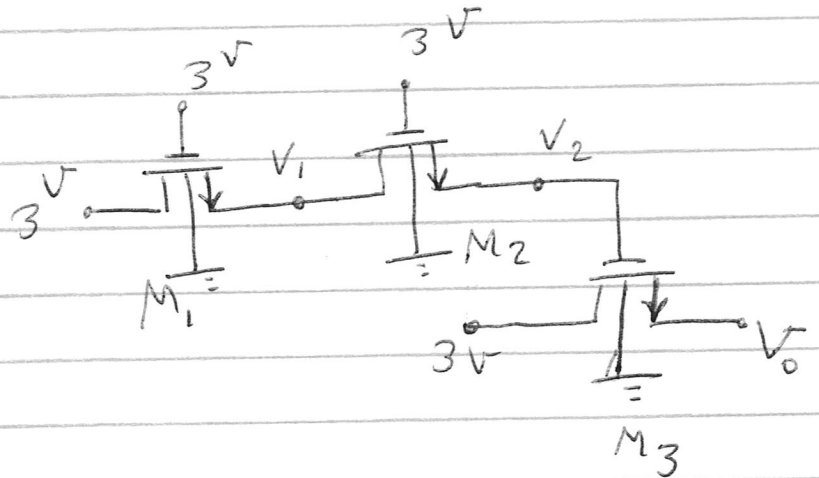
$$t_{pLH} = 0.7 R_{OH} \cdot C_L \rightarrow R_{OH} = \frac{t_{pLH}}{0.7 C_L} = \frac{11.05 \text{ ns}}{0.7 (100 \text{ pF})} \quad R_{OH} = 1579 \Omega$$

Problem 2:
Based on Elmore technique:

$$\begin{aligned} \tau &= (1.579 \text{ k}\Omega + 120 \Omega) 75 \text{ fF} \\ &+ (1.579 \text{ k}\Omega + 120 \Omega) (15 \text{ fF} + 75 \text{ fF} + 50 \text{ fF}) \\ &+ (1.579 \text{ k}\Omega + 120 \Omega + 165 \Omega) 100 \text{ pF} \\ &+ (1.579 \text{ k}\Omega + 120 \Omega + 165 \Omega) 95 \text{ fF} \\ &+ (1.579 \text{ k}\Omega + 120 \Omega + 165 \Omega + 105 \Omega) 65 \text{ fF} \\ &+ (1.579 \text{ k}\Omega + 120 \Omega + 165 \Omega + 105 \Omega + 220 \Omega) 200 \text{ pF} \end{aligned}$$

$$\begin{aligned} \tau &= 1.24 \text{ ns} \\ \rightarrow t_{pLH} &= 0.7 \tau = 0.7 \times 1.24 \text{ ns} = 871 \text{ ps} \end{aligned}$$

3) $V_{T0} = 0.5 \text{ V}$
 $\gamma = 0.3 \text{ V}^{1/2}$
 $|K_{PF}| = 0.35$



For M_1 :

$$\begin{cases} V_1 = 3 - V_{T1} \\ V_{T1} = V_{T0} + \gamma (\sqrt{2\phi_F + V_{SB}} - \sqrt{2\phi_F}) \\ V_{SB} = V_1 \end{cases}$$

$$\Rightarrow V_1 = 3 - \left\{ 0.5 + 0.3 (\sqrt{0.7 + V_1} - \sqrt{0.7}) \right\}$$

Solve for $V_1 \Rightarrow \underline{V_1 = 2.23687 \text{ V}}$

For M_2 : (Same as M_1) $\underline{V_2 = V_1 = 2.23687 \text{ V}}$

For M_3 :

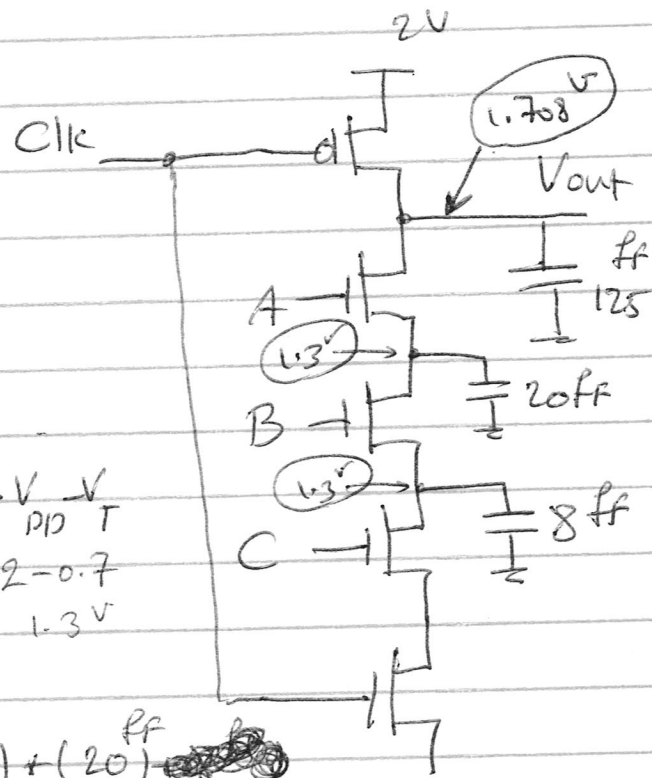
$$\begin{cases} V_0 = V_2 - V_{T3} \\ V_{T3} = V_{T0} + \gamma (\sqrt{2\phi_F + V_{SB}} - \sqrt{2\phi_F}) \\ V_{SB} = V_0 \end{cases}$$

$$\Rightarrow V_0 = 2.23687 - \left\{ 0.5 + 0.3 (\sqrt{0.7 + V_0} - \sqrt{0.7}) \right\}$$

Solve for $V_0 \Rightarrow \underline{V_0 = 1.53898 \text{ V}}$

4) $V_A = V_B = 2V$, $V_C = 0$

$Q_{initial} = 125 \text{ ff} \times 2V = 250 \text{ fc}$



Case 1: $Q_{final} = (125 + 20 + 8) \times V_x$

$Q_{initial} = Q_{final} \Rightarrow V_x = 1.634V$
 impossible $\rightarrow \begin{matrix} V_{DD} - V_T \\ 2 - 0.7 \\ 1.3V \end{matrix}$

Case 2: $Q_{final} = 125 \text{ ff} \times V_x + (V_{DD} - V_T) \times (20) + (V_{DD} - V_T) \times (8)$

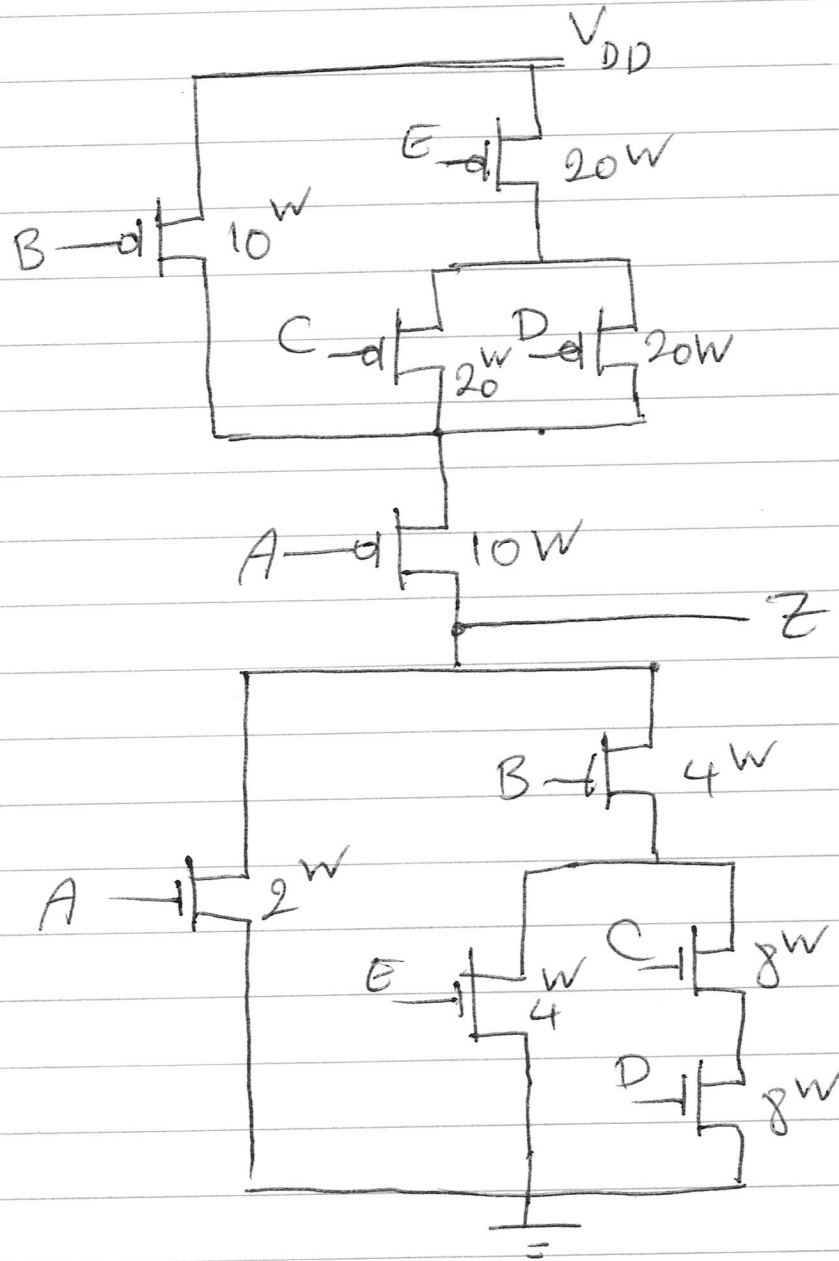
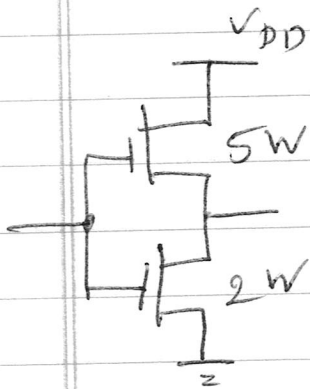
$= 125 \text{ ff} \times V_x + 1.3V \times 20 \text{ ff} + 1.3V \times 8 \text{ ff} = 125 \text{ ff} V_x + 36.4 \text{ fc}$

$Q_{initial} = Q_{final} \Rightarrow 125 \text{ ff} V_x + 36.4 \text{ ff} = 250 \text{ ff}$

$\Rightarrow V_x = 1.7088V$

- $V_{125 \text{ ff}} = 1.7088V$
- $V_{20 \text{ ff}} = 1.3V$
- $V_{8 \text{ ff}} = 1.3V$

5)



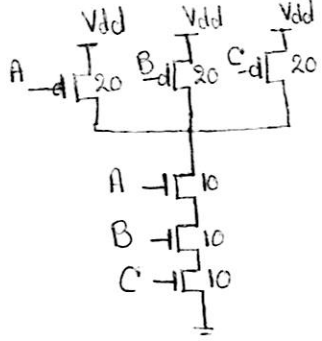
$$6) P_{\text{leakage}} = \frac{1}{2} V_{DD} [I_{\text{off}}(\text{nmos}) + I_{\text{off}}(\text{pmos})]$$

$$W_n = 58.5 \mu\text{m} \rightsquigarrow I_{\text{off}}(\text{nmos}) = 702 \text{ nA}$$

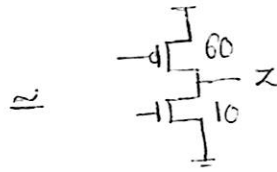
$$W_p = 85.5 \mu\text{m} \rightsquigarrow I_{\text{off}}(\text{pmos}) = 2223 \text{ nA}$$

$$\Rightarrow P_{\text{leakage}} = \frac{1}{2} * 0.5 \text{ V} * (702 \text{ nA} + 2223 \text{ nA})$$

$$\Rightarrow P_{\text{leakage}} = 1.755 \text{ mW}$$



Problem 7:



For nMOS: $W_{eq} = \frac{1}{1/30 + 1/30 + 1/30} = 10$

For pMOS: $W_{eq} = 20 + 20 + 20 = 60$

1) 000 → III

The 3 nMOS transistors in series are on and the p-channel transistors are off hence, the current across the nMOS transistor is:

$$I = \frac{K'_n}{2} \left(\frac{W}{L}\right) (V_{gs} - V_{tn})^2 \text{ where } V_{gs} = V_{dd}$$

$$I = \frac{100 \mu A/V^2}{2} (10) (1.5V - 0.7V)^2 = 605 \mu A$$

$$t_{PLH} = \frac{C_L V_{dd}/2}{I} = \frac{(100 fF)(1.5V/2)}{605 \mu A} = 123.97 \text{ ps}$$

2) III → I10

The pMOS transistor with C input will be on and the other pMOS transistors will be off, then the current for the pMOS transistor is:

$$I = \frac{K'_p}{2} (W/L) (V_{gs} - |V_{tp}|)^2$$

$$I = \frac{60 \mu A/V^2}{2} (20) (1.5 - 0.7)^2 = 726 \mu A$$

$$t_{PLH} = \frac{C_L V_{dd}/2}{I_p} = \frac{(100 fF)(1.5V/2)}{726 \mu A} = 103.31 \text{ ps}$$

3) III → 100

The pMOS transistor with B input and A input will be on and the nMOS transistor with A input will be on; then, the current for the pMOS transistor is:

$$I_p = \frac{K'_p}{2} (W/L) (V_{gs} - |V_{tp}|)^2$$

$$I_p = \frac{60 \mu A/V^2}{2} (40) (1.5V - 0.7V)^2 = 1452 \mu A$$

$$t_{PLH} = \frac{C V_{dd}/2}{I_p} = \frac{(100 fF)(1.5V/2)}{1452 \mu A} = 51.65 \text{ ps}$$

f) $111 \rightarrow 000$ All the pMOS transistors are on and all the nMOS transistors are off; then, the current for the pMOS transistor is:

$$-4 \frac{1}{60}$$

$$I_p = \frac{k_p}{2} (W/L) (V_{gs} - |V_{tp}|)^2$$

$$I_p = \frac{60 \mu A/V^2}{2} (60) (1.5V - 0.7V)^2 = 2178 \mu A$$

$$t_{pLH} = \frac{C V_{dd}/2}{I_p} = \frac{(100 fF) (1.5V/2)}{2178 \mu A} = 34.43 ps$$

b) $t_{pd} = 0.7RC \rightarrow R = \frac{t_{pd}}{0.7C}$

1) $000 \rightarrow 111$ $R_{eff} = \frac{123.97 ps}{0.7(100 fF)} = 1771 \Omega$

2) $111 \rightarrow 110$ $R_{eff} = \frac{103.31 ps}{0.7(100 fF)} = 1475.86 \Omega$

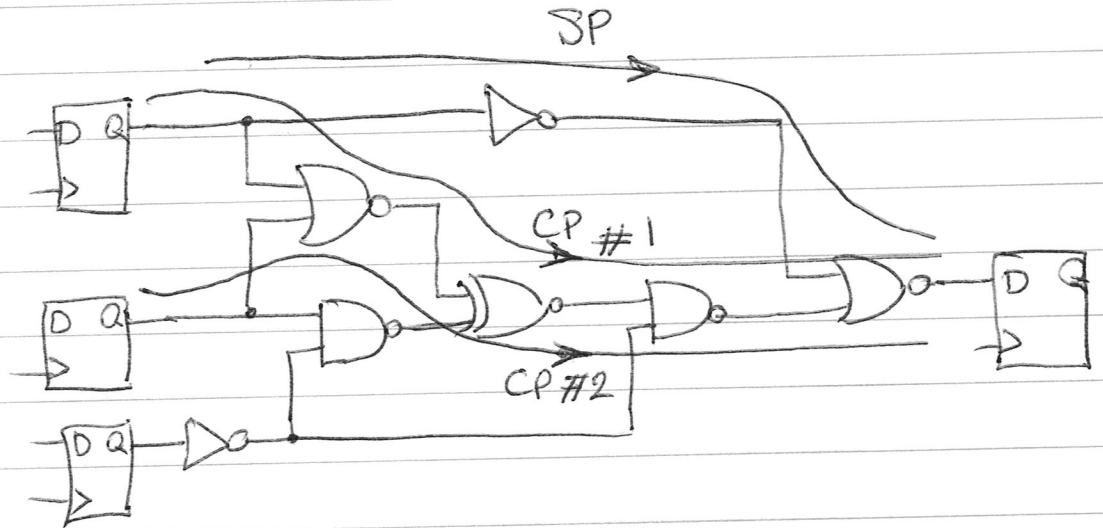
3) $111 \rightarrow 100$ $R_{eff} = \frac{51.65 ps}{0.7(100 fF)} = 737.86 \Omega$

4) $111 \rightarrow 000$ $R_{eff} = \frac{34.43 ps}{0.7(100 fF)} = 491.86 \Omega$

Transition (ABC)	Propagation Delay	Effective Output Resistance
$000 \rightarrow 111$	123.97 ps	1771 Ω
$111 \rightarrow 110$	103.31 ps	1475.86 Ω
$111 \rightarrow 100$	51.65 ps	737.86 Ω
$111 \rightarrow 000$	34.43 ps	491.86 Ω

8)

a)



b)

There are 2 critical paths (CP#1, CP#2) and one short path (SP)

$$T > t_{cqa} + t_{logic} + t_{setup}$$

$$T > 1^{ns} + (550^{ps} + 750^{ps} + 350^{ps} + 450^{ps}) + 3^{ns}$$

$$T > 6.1^{ns} \Rightarrow f_{max} = \frac{1}{T_{min}} = \frac{1}{6.1^{ns}} = 163.93 \text{ MHz}$$

c) SP is the short path.

d) $t_{hold} < t_{cqa} + t_{logic, CD}$

$$2^{ns} < 1^{ns} + (150^{ps} + 450^{ps})$$

~~$2^{ns} < 1.6^{ns}$~~ This is a hold time violation

9) a) $Z = \overline{A(BC+DE)}$

b) ABCDE = 11010

c) if ABCDE = 11000, $V_{DD} = 1.2V$

$$\left. \begin{aligned} Q_{initial} &= 20 \text{ fF} * 1.2V \\ Q_{final} &= (20 \text{ fF} + 3 \text{ fF} + 2 \text{ fF}) * V_x \end{aligned} \right\} \Rightarrow V_x = 0.96 > 0.8V \text{ impossible}$$

$$V_{DD} - V_T = 1.2 - 0.4 = 0.8V$$

rework
=>

$$\left. \begin{aligned} Q_{initial} &= 20 \text{ fF} * 1.2V \\ Q_{final} &= 20 \text{ fF} * V_x + 3 \text{ fF} * (1.2 - 0.4) + 2 \text{ fF} * (1.2 - 0.4) \end{aligned} \right\}$$

$V_x = 1.0V$ ✓

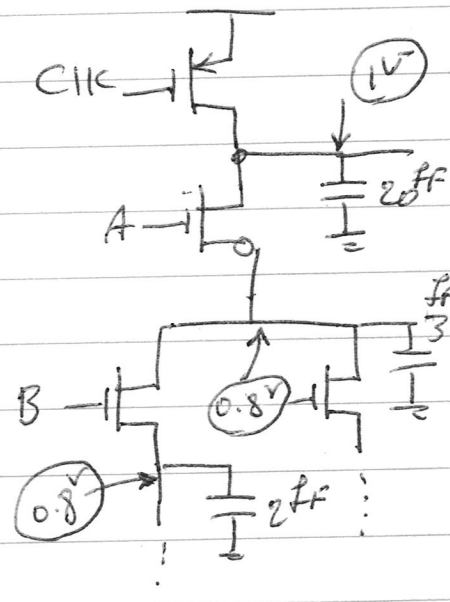
d) $t_{0 \rightarrow 90\%} = 250 \text{ ps}$

$\Delta V = 0.9 * 1.2V = 1.08V$

$C_{total} = 20 + (3 + 2 + 2 + 3) = 30 \text{ fF}$

$$I = \frac{K'_P}{2} \left(\frac{W}{L}\right)_P (V_{GS} - V_{TP})^2$$

$$= \frac{50 \text{ MA/V}^2}{2} \left(\frac{W}{L}\right)_P (1.2 - 0.5)^2$$



$$\Rightarrow 250 \text{ ps} = \frac{30 \text{ fF} * (1.08V)}{\frac{50 \text{ MA/V}^2}{2} \left(\frac{W}{L}\right)_P (1.2 - 0.5)^2} \Rightarrow \left(\frac{W}{L}\right)_P = 10.58$$

$W_P = 1.058 \mu\text{m}$
or $L = 100 \text{ nm}$

10)

